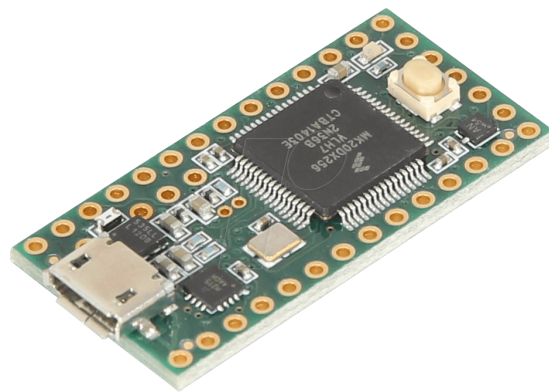


Teensy 3.2



Technical Specification

Model	Teensy 3.2
Clock Frequency	72 MHz
Bit-Architecture	32-Bit
Microcontroller	MK20DK256
Flash-Memory	256 kB
SRAM	64 kB
EEPROM	2
Voltage	5V
Digital I/O Pins	34
thereof with PWM	12
Other connections	USB, SPI, 2x I2C, 3x UART, CAN
EAN	4250236812014